

ds-7216huhi -k2. txt

U-Boot 2010.06-svn (Mar 24 2017 - 17:18:44)[V1.4.5]

```
Hit ctrl+u to stop autoboot: 0
### CRAMFS load complete: 3809712 bytes loaded to 0x42000000
## Booting kernel from Legacy Image at 42000000 ...
   Verifying RSA ... OK
   Loading Kernel Image ... OK
OK
```

Starting kernel ...

```
Sat Mar  2 21:16:51 UTC 2019
Starting udev: [ OK ]
sdbg: server ip: 192.0.0.128
fastboot is
language is 1
devtype is ab6f
BusyBox v1.2.1 Protect Shell (psh)
Enter 'help' for a list of davinci system commands.
```

```
# /
-----<1> tar guir webs -----
-----<2> show logo -----
ERROR this is a new device, please check!!
scriptCmd: ./load3531d -i 0.
a is ab6f
DS_7216HUH_K
mmz allot OK DS_7216HUH_K
loadKY2017 Start
*** Board tools : ver0.0.1_20121120 ***
[debug]: {source/utills/cmdshell.c:166}cmdstr: hi mm
0x120F0848: 0x00000050 --> 0x00000038
[END]
*** Board tools : ver0.0.1_20121120 ***
[debug]: {source/utills/cmdshell.c:166}cmdstr: hi mm
0x120F0894: 0x00000050 --> 0x00000038
[END]
*** Board tools : ver0.0.1_20121120 ***
[debug]: {source/utills/cmdshell.c:166}cmdstr: hi mm
0x120F08E0: 0x00000050 --> 0x00000038
[END]
*** Board tools : ver0.0.1_20121120 ***
[debug]: {source/utills/cmdshell.c:166}cmdstr: hi mm
0x120F092C: 0x00000050 --> 0x00000038
[END]
check adNum
tp2802: 0, tp2806: 0, tp2822: 0, tp2823: 0, tp2824: 0, tp2833C: 2, tw2964: 0
tw2968: 0 nvp6114a: 0 nvp6124b: 0 nvp6134: 2, nvp6134b: 0
cmdBuf: insmod extdrv/tp2823.ko I2C_dev_Chose=1
cmdBuf: insmod extdrv/nvp6134_ex.ko I2C_dev_Chose=1
insert audio
load pci e OK
load K71 ok
/
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```
-----<4> del no use res -----
cp: can't stat '/home/app/hisi/modules/setDebug_mux': No such file
or directory
iSCSI daemon with pid=341 started! Can't open parameter file on
flash, errno=0x2.
getPPPOECfg: from /home/app/config/devCfg.bin ok.
remove ppoed!
=====enablePPPoE failed!!!
fun: hisi_main_module_init, line: 7847 >>>>>ATA is Not Scan Done!
Please Wait<<<<<<
pdev_hard_info->ataCtrlNums=2
file: src/pal_api.c, line: 497, pal_module_init successfully
file: src/pal_api.c, line: 458, pal_module_init has been called
remove error
: 1
fun: hisi_watchdog_init, line: 3819
hisi_watchdog_init(/dev/watchdog, 28) ok iDogTimeOut: 25 Sec!
/*****init_global_error_lib*/,
init_global_error_lib start!
reset_phy start =====
~~~~~phy on eth0 is link up
-----get_net_link status == 1
t1: wait storage [2] and gui [2] and hcore [2] and ipcm [0] and
tool server [0]
: 1
[DHCP] default plpAddr: 192.168.1.64, pNetMask: 255.255.255.0
fun: hisi_dsp_init, line: 2427 -----init dsp
-----
<LIB>DspInitParaAllLocMem OK! addr is 0xb3fbf000
fun: hisi_main_dsp_init, line: 1669 Device Main Vo Chan is 2[0]
menuDispChan = 0
--- ana + IP 16 + 2
hisi_dsp_net_mem_proc -----chan[0]=1
In hisi_dsp_net_mem_allloc, nChan = 1[16], nChanType= 4,
anolEnableBit = 65535
chan = 0, type = 4
NetPool Main chan[0]chanType[4].vAddr =0xb059f000
NetPool Main chan[0]chanType[4].totalLen =0x200000
hisi_dsp_net_mem_proc -----chan[0]=1
In hisi_dsp_net_mem_allloc, nChan = 1[16], nChanType= 5,
anolEnableBit = 65535
chan = 0, type = 5
NetPool Main chan[0]chanType[5].vAddr =0xb051f000
NetPool Main chan[0]chanType[5].totalLen =0x80000
--- ana + IP 16 + 2
hisi_dsp_net_mem_proc -----chan[1]=1
In hisi_dsp_net_mem_allloc, nChan = 2[16], nChanType= 4,
anolEnableBit = 65535
chan = 1, type = 4
NetPool Main chan[1]chanType[4].vAddr =0xb031f000
NetPool Main chan[1]chanType[4].totalLen =0x200000
hisi_dsp_net_mem_proc -----chan[1]=1
In hisi_dsp_net_mem_allloc, nChan = 2[16], nChanType= 5,
anolEnableBit = 65535
chan = 1, type = 5
NetPool Main chan[1]chanType[5].vAddr =0xb029f000
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NetPool Main chan[1]chanType[5]. total Len =0x80000
--- ana + IP 16 + 2
hi si_dsp_net_mem_proc -----chan[2]=1
In hi si_dsp_net_mem_all oc, nChan = 3[16], nChanType= 4,
anol EnableBit = 65535
chan = 2, type = 4
NetPool Main chan[2]chanType[4]. vAddr =0xb009f000
NetPool Main chan[2]chanType[4]. total Len =0x200000
hi si_dsp_net_mem_proc -----chan[2]=1
In hi si_dsp_net_mem_all oc, nChan = 3[16], nChanType= 5,
anol EnableBit = 65535
chan = 2, type = 5
NetPool Main chan[2]chanType[5]. vAddr =0xb001f000
NetPool Main chan[2]chanType[5]. total Len =0x80000
--- ana + IP 16 + 2
hi si_dsp_net_mem_proc -----chan[3]=1
In hi si_dsp_net_mem_all oc, nChan = 4[16], nChanType= 4,
anol EnableBit = 65535
chan = 3, type = 4
NetPool Main chan[3]chanType[4]. vAddr =0xafe1f000
NetPool Main chan[3]chanType[4]. total Len =0x200000
hi si_dsp_net_mem_proc -----chan[3]=1
In hi si_dsp_net_mem_all oc, nChan = 4[16], nChanType= 5,
anol EnableBit = 65535
chan = 3, type = 5
NetPool Main chan[3]chanType[5]. vAddr =0xafd9f000
NetPool Main chan[3]chanType[5]. total Len =0x80000
--- ana + IP 16 + 2
hi si_dsp_net_mem_proc -----chan[4]=1
In hi si_dsp_net_mem_all oc, nChan = 5[16], nChanType= 4,
anol EnableBit = 65535
chan = 4, type = 4
NetPool Main chan[4]chanType[4]. vAddr =0xafb9f000
NetPool Main chan[4]chanType[4]. total Len =0x200000
hi si_dsp_net_mem_proc -----chan[4]=1
In hi si_dsp_net_mem_all oc, nChan = 5[16], nChanType= 5,
anol EnableBit = 65535
chan = 4, type = 5
NetPool Main chan[4]chanType[5]. vAddr =0xafb1f000
NetPool Main chan[4]chanType[5]. total Len =0x80000
--- ana + IP 16 + 2
hi si_dsp_net_mem_proc -----chan[5]=1
In hi si_dsp_net_mem_all oc, nChan = 6[16], nChanType= 4,
anol EnableBit = 65535
chan = 5, type = 4
NetPool Main chan[5]chanType[4]. vAddr =0xaf91f000
NetPool Main chan[5]chanType[4]. total Len =0x200000
hi si_dsp_net_mem_proc -----chan[5]=1
In hi si_dsp_net_mem_all oc, nChan = 6[16], nChanType= 5,
anol EnableBit = 65535
chan = 5, type = 5
NetPool Main chan[5]chanType[5]. vAddr =0xaf89f000
NetPool Main chan[5]chanType[5]. total Len =0x80000
--- ana + IP 16 + 2
hi si_dsp_net_mem_proc -----chan[6]=1
In hi si_dsp_net_mem_all oc, nChan = 7[16], nChanType= 4,
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anol EnableBit = 65535
chan = 6, type = 4
NetPool Main chan[6]chanType[4]. vAddr =0xaf69f000
NetPool Main chan[6]chanType[4]. total Len =0x200000
hi si_dsp_net_mem_proc -----chan[6]=1
In hi si_dsp_net_mem_all oc, nChan = 7[16], nChanType= 5,
anol EnableBit = 65535
chan = 6, type = 5
NetPool Main chan[6]chanType[5]. vAddr =0xaf61f000
NetPool Main chan[6]chanType[5]. total Len =0x80000
--- ana + IP 16 + 2
hi si_dsp_net_mem_proc -----chan[7]=1
In hi si_dsp_net_mem_all oc, nChan = 8[16], nChanType= 4,
anol EnableBit = 65535
chan = 7, type = 4
NetPool Main chan[7]chanType[4]. vAddr =0xaf41f000
NetPool Main chan[7]chanType[4]. total Len =0x200000
hi si_dsp_net_mem_proc -----chan[7]=1
In hi si_dsp_net_mem_all oc, nChan = 8[16], nChanType= 5,
anol EnableBit = 65535
chan = 7, type = 5
NetPool Main chan[7]chanType[5]. vAddr =0xaf39f000
NetPool Main chan[7]chanType[5]. total Len =0x80000
--- ana + IP 16 + 2
hi si_dsp_net_mem_proc -----chan[8]=1
In hi si_dsp_net_mem_all oc, nChan = 9[16], nChanType= 4,
anol EnableBit = 65535
chan = 8, type = 4
NetPool Main chan[8]chanType[4]. vAddr =0xaf19f000
NetPool Main chan[8]chanType[4]. total Len =0x200000
hi si_dsp_net_mem_proc -----chan[8]=1
In hi si_dsp_net_mem_all oc, nChan = 9[16], nChanType= 5,
anol EnableBit = 65535
chan = 8, type = 5
NetPool Main chan[8]chanType[5]. vAddr =0xaf11f000
NetPool Main chan[8]chanType[5]. total Len =0x80000
--- ana + IP 16 + 2
hi si_dsp_net_mem_proc -----chan[9]=1
In hi si_dsp_net_mem_all oc, nChan = 10[16], nChanType= 4,
anol EnableBit = 65535
chan = 9, type = 4
NetPool Main chan[9]chanType[4]. vAddr =0xaeef1f000
NetPool Main chan[9]chanType[4]. total Len =0x200000
hi si_dsp_net_mem_proc -----chan[9]=1
In hi si_dsp_net_mem_all oc, nChan = 10[16], nChanType= 5,
anol EnableBit = 65535
chan = 9, type = 5
NetPool Main chan[9]chanType[5]. vAddr =0xaeef9f000
NetPool Main chan[9]chanType[5]. total Len =0x80000
--- ana + IP 16 + 2
hi si_dsp_net_mem_proc -----chan[10]=1
In hi si_dsp_net_mem_all oc, nChan = 11[16], nChanType= 4,
anol EnableBit = 65535
chan = 10, type = 4
NetPool Main chan[10]chanType[4]. vAddr =0xae9f000
NetPool Main chan[10]chanType[4]. total Len =0x200000
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hi si_dsp_net_mem_proc -----chan[10]=1
In hi si_dsp_net_mem_all oc, nChan = 11[16], nChanType= 5,
anol EnableBit = 65535
chan = 10, type = 5
NetPool Main chan[10]chanType[5].vAddr =0xae1f000
NetPool Main chan[10]chanType[5].total Len =0x80000
--- ana + IP 16 + 2
hi si_dsp_net_mem_proc -----chan[11]=1
In hi si_dsp_net_mem_all oc, nChan = 12[16], nChanType= 4,
anol EnableBit = 65535
chan = 11, type = 4
NetPool Main chan[11]chanType[4].vAddr =0xae1f000
NetPool Main chan[11]chanType[4].total Len =0x200000
hi si_dsp_net_mem_proc -----chan[11]=1
In hi si_dsp_net_mem_all oc, nChan = 12[16], nChanType= 5,
anol EnableBit = 65535
chan = 11, type = 5
NetPool Main chan[11]chanType[5].vAddr =0xae99f000
NetPool Main chan[11]chanType[5].total Len =0x80000
--- ana + IP 16 + 2
hi si_dsp_net_mem_proc -----chan[12]=1
In hi si_dsp_net_mem_all oc, nChan = 13[16], nChanType= 4,
anol EnableBit = 65535
chan = 12, type = 4
NetPool Main chan[12]chanType[4].vAddr =0xae79f000
NetPool Main chan[12]chanType[4].total Len =0x200000
hi si_dsp_net_mem_proc -----chan[12]=1
In hi si_dsp_net_mem_all oc, nChan = 13[16], nChanType= 5,
anol EnableBit = 65535
chan = 12, type = 5
NetPool Main chan[12]chanType[5].vAddr =0xae71f000
NetPool Main chan[12]chanType[5].total Len =0x80000
--- ana + IP 16 + 2
hi si_dsp_net_mem_proc -----chan[13]=1
In hi si_dsp_net_mem_all oc, nChan = 14[16], nChanType= 4,
anol EnableBit = 65535
chan = 13, type = 4
NetPool Main chan[13]chanType[4].vAddr =0xae51f000
NetPool Main chan[13]chanType[4].total Len =0x200000
hi si_dsp_net_mem_proc -----chan[13]=1
In hi si_dsp_net_mem_all oc, nChan = 14[16], nChanType= 5,
anol EnableBit = 65535
chan = 13, type = 5
NetPool Main chan[13]chanType[5].vAddr =0xae49f000
NetPool Main chan[13]chanType[5].total Len =0x80000
--- ana + IP 16 + 2
hi si_dsp_net_mem_proc -----chan[14]=1
In hi si_dsp_net_mem_all oc, nChan = 15[16], nChanType= 4,
anol EnableBit = 65535
chan = 14, type = 4
NetPool Main chan[14]chanType[4].vAddr =0xae29f000
NetPool Main chan[14]chanType[4].total Len =0x200000
hi si_dsp_net_mem_proc -----chan[14]=1
In hi si_dsp_net_mem_all oc, nChan = 15[16], nChanType= 5,
anol EnableBit = 65535
chan = 14, type = 5
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NetPool Main chan[14]chanType[5].vAddr =0xae21f000
NetPool Main chan[14]chanType[5].total Len =0x80000
--- ana + IP 16 + 2
hi si_dsp_net_mem_proc -----chan[15]=1
In hi si_dsp_net_mem_all oc, nChan = 16[16], nChanType= 4,
anol EnableBit = 65535
eth0: ip = 192.168.1.104, subnet = 255.255.255.0, router =
192.168.1.1, dns = 10.17.0.103 192.168.1.1
chan = 15, type = 4
NetPool Main chan[15]chanType[4].vAddr =0xae01f000
NetPool Main chan[15]chanType[4].total Len =0x200000
hi si_dsp_net_mem_proc -----chan[15]=1
In hi si_dsp_net_mem_all oc, nChan = 16[16], nChanType= 5,
anol EnableBit = 65535
chan = 15, type = 5
NetPool Main chan[15]chanType[5].vAddr =0xadf9f000
NetPool Main chan[15]chanType[5].total Len =0x80000
--- ana + IP 16 + 2
hi si_dsp_net_mem_proc -----chan[16]=1
In hi si_dsp_net_mem_all oc, nChan = 17[16], nChanType= 2,
anol EnableBit = 65535
IpcNetPool chan[0]chanType[2].vAddr =0xade5f000
IpcNetPool chan[0]chanType[2].total Len =0x140000
hi si_dsp_net_mem_proc -----chan[16]=1
In hi si_dsp_net_mem_all oc, nChan = 17[16], nChanType= 3,
anol EnableBit = 65535
IpcNetPool chan[0]chanType[3].vAddr =0xade1f000
IpcNetPool chan[0]chanType[3].total Len =0x40000
--- ana + IP 16 + 2
hi si_dsp_net_mem_proc -----chan[17]=1
In hi si_dsp_net_mem_all oc, nChan = 18[16], nChanType= 2,
anol EnableBit = 65535
IpcNetPool chan[1]chanType[2].vAddr =0xadcdf000
IpcNetPool chan[1]chanType[2].total Len =0x140000
hi si_dsp_net_mem_proc -----chan[17]=1
In hi si_dsp_net_mem_all oc, nChan = 18[16], nChanType= 3,
anol EnableBit = 65535
IpcNetPool chan[1]chanType[3].vAddr =0xadc9f000
IpcNetPool chan[1]chanType[3].total Len =0x40000
g_pDspl ni tPara->exNetPool Main.vAddr =0xad69b000
g_pDspl ni tPara->exNetPool Main.total Len =0x80000
g_pDspl ni tPara->exRecPool [0].strAddr[0].vAddr =0xad49a000
g_pDspl ni tPara->exRecPool [0].strAddr[0].total Len =0x200000
<LIB>*** [APP_PROCESS] DSP_LIB Build Time: [Mar 28 2017 19: 22: 58]
*** sizeof(DSPINITPARA) 3107008
<DSP> pos error ---[InitPosMem] err, not support pos---
t1: wait storage [2] and gui [2] and hi core[2] and ipcm[0] and
tool server[0]
111vcaVpssGroup = 37
111vcaVpssGroup = 38
<DSP>vca DspVcal ni t0sd OK!
<DSP>vca Init vca success
<DSP>vca DspVcal ni tMetaDataPack: ok
<DSP>vca DspVcaScdInit chanNum = 2
<DSP>vca scdChan: 0
<DSP>vca scdChan: 1
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<DSP>vca DspVcaScdI n i t OK!
<DSP>vca smdChan: 0
<DSP>vca smdChan: 1
<LIB>AppA l l ocLogoShareBuf 1. u32Total ShareBufSi ze: 10551296
ShareLogoVi rAddr: 0xac888000
<LIB>AppA l l ocLogoShareBuf 2. u32Total ShareBufSi ze: 10616832
ShareExtLogoVi rAddr: 0xac878000
<LIB>AppA l l ocLogoShareBuf i s OK
<LIB>1. Share Buffer Si ze 44433408
<LIB>2. Share Buffer Si ze 49348608
<LIB>3. Share Buffer Si ze 52690944
<LIB><AppA l l ocMenuMem> di spMode 5
<LIB>chan 0, menuShareBuf vi rtRgbAddr i s 0xa9086000, menuBufSi ze
16588800
<LIB>chan 1, menuShareBuf vi rtRgbAddr i s 0xa8fbb000, menuBufSi ze
829440
<LIB>DspA l l ocShareMemSi ze OK Total Share Buffer Si ze 70109184
<LIB>pi d: 346 ti d: 488 name: tskPI usDataFunc

<LIB>startMagi c : DSP_START_OK
hi si _mai n_ dsp_ i n i t: DSPI NI TPARA=3107008
*****g_pDspI n i tPara[0xb3fbf000], menuShareBuf 0[0xb402f074,
pAddr=0xa3ffc000, vAddr=0xa9086000, w: 3840, h2160, p: 3840]
1[0xb402f088, pAddr=0xa4fce000, vAddr=0xa8fbb000,
w: 720, h576, p: 720],
hi si _freelogo_buffer i s succsse 0
fun: hi si _dsp_ i n i t, line: 2448 ----- dsp i s i n i t Ok
iRetVal : 0-----
!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!
fun: hi si _fi ll _16_ screen_ param, line: 1883 wi ndow di spl nfo[w: 1280,
h: 1024], step(320, 256), last(320, 256)
fun: hi si _fi ll _16_ screen_ param, line: 1883 wi ndow di spl nfo[w: 704,
h: 576], step(176, 144), last(176, 144)
t1: wait storage [2] and gui [2] and hi core[2] and i pcm[0] and
tool server[0]
t1: wait storage [2] and gui [2] and hi core[2] and i pcm[0] and
tool server[0]
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tool server[0]
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